

\* (1) In schema originala pinii 1,9,10,11,15 de la U08 erau lasati in aer, doar 4 era legat la "1"

\* (1) In the original schematics pins 1,9,10,11,15 of U08 were not connected, only 4 was at "1"

**TITLE** Interfata disc flexibil – Circuite formare ceas scriere si PLL digital  
Floppy disk interface – Write clock and digital PLL circuits

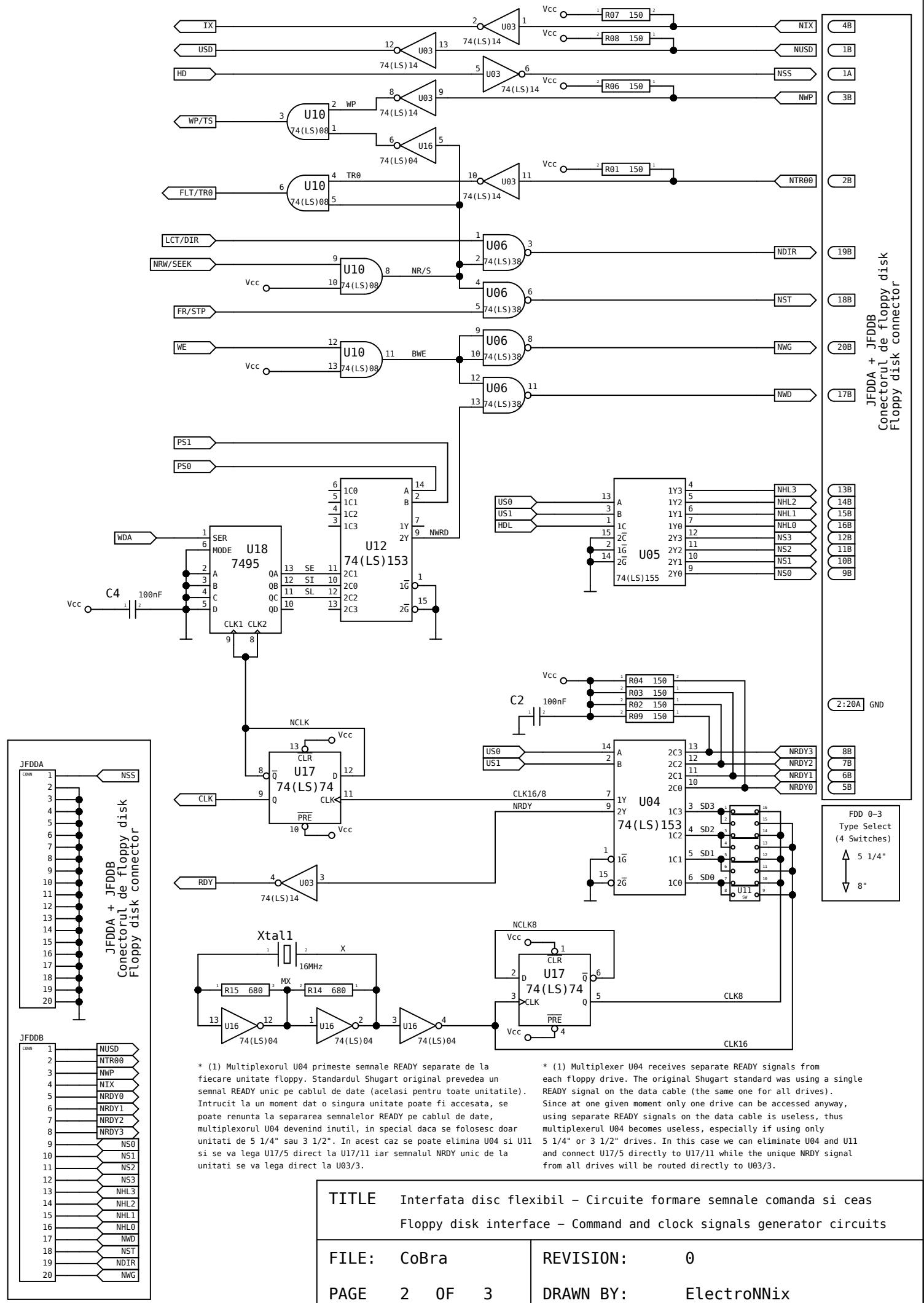
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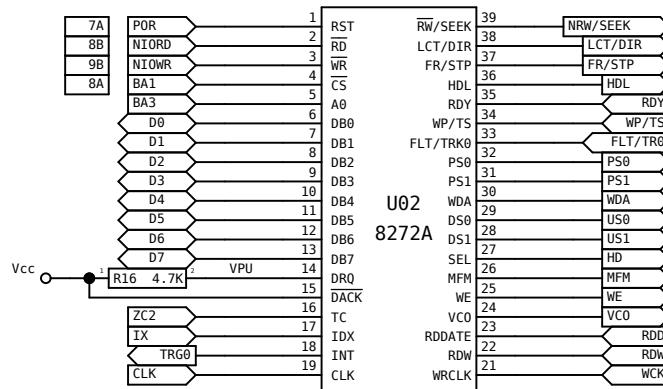
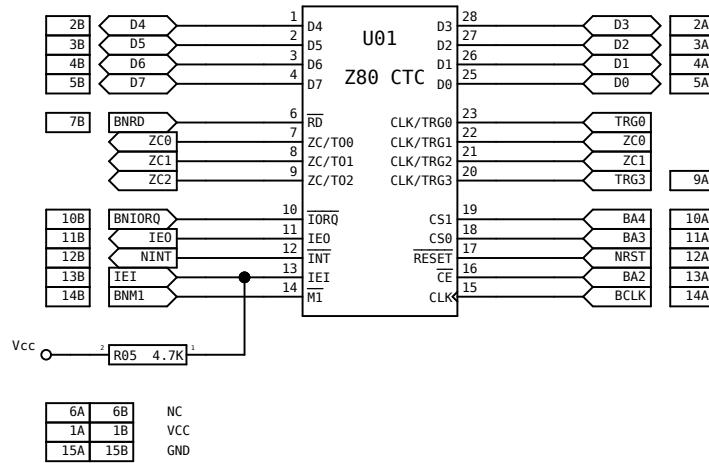
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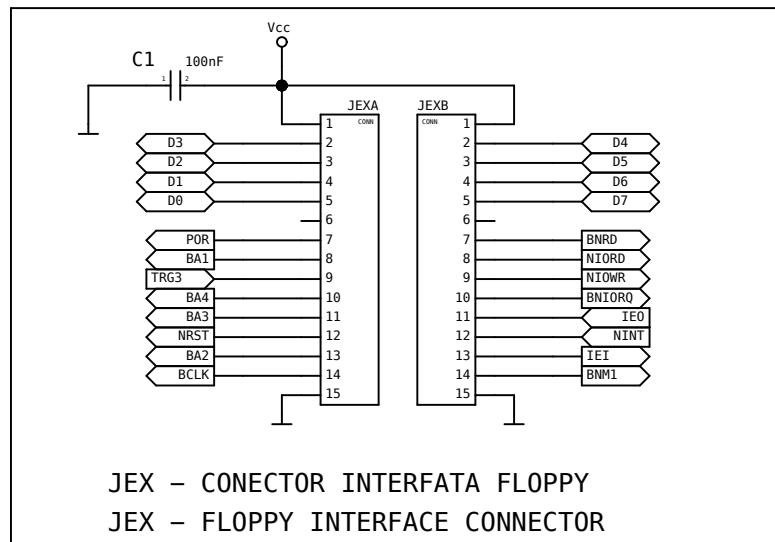
JFDDA + JFDBB  
Conectorul de floppy disk  
Floppy disk connector





\* (1) Manualul original avea JEXA/8 listat ca fiind legat la BA7. De asemenea cablajul original avea JEXA/8 legat la BA7. Corect este ca JEXA/8 sa fie legat la BA1. Am modificat deci atit schema de fata precum si cablajul legind JEXA/8 la BA1 (pe placă de baza).

\* (1) The original hardware manual had JEXA/8 listed as being connected to BA7. Also the original mainboard layout had JEXA/8 connected to BA7. Correctly, JEXA/8 should be connected to BA1. I have therefore changed this schematic as well as the mainboard layout by connecting JEXA/8 to BA1.



TITLE Interfata disc flexibil - Controlorul de disc  
Floppy disk interface - Disk controller

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